QP CODE: 19102139

19102139

Reg No : .....

Name : .....

B.Sc. DEGREE (CBCS) EXAMINATION, OCTOBER 2019

Third Semester

B.Sc Electronics Model III

CORE COURSE - EL3CRT09 - 8085 MICROPROCESSOR

2017 Admission Onwards

C13087A2

Maximum Marks: 80 Time: 3 Hours

Part A

*Answer any **ten** questions.*

Each question carries 2 marks.

1. Define the term byte.
2. What is address bus ?Why it is unidirectional?
3. Define registers.
4. Explain the function of zero flag.
5. What is the need for timing diagram?
6. Explain the instruction CMP.
7. Explain the instruction JNC.
8. Whether RIM is a machine control instruction used in 8085. Justify your answer.
9. Explain the instruction IN.
10. Define interrupt.
11. Define maskable and non maskable interrupts.
12. What is the mode zero in 8255?

(10×2=20)

Part B

*Answer any **six** questions.*

Each question carries 5 marks.

13. Explain any external data operation of 8085.

14. Explain demultiplexing of address bus.

15. Explain any 5 timing and control signals of 8085 microprocessor.

16. Explain implicit addressing mode.

17. Explain the following instructions 1) ADI 2) SUI.

18. What are the sequence of events associated with RET instruction.

19. Explain the need for I/O ports in 8085.

20. Write a program to produce time delay using 1 register. Draw the flow chart for the program also.

21. How can you interface A/D converter to 8085 microprocessor using interrupt?

(6×5=30)

Part C

Answer any two questions.

Each question carries 15 marks.

22. With detailed diagrams explain the architecture of 8085 in detail.

23. Explain with examples ,any five data transfer group of instructions.

24. Write a subroutine to check whether a given byte in register Accumulator is even or odd. If it is even/odd, register B should contain 00H/FFH respectively.

25. Explain in detail the control word,control logic and modes of operation of 8255.

(2×15=30)